



Patent
Attorney's Docket No. 027260-481

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Patent Application of)

Michio KOMODA)

Application No.: 09/921,604)

Filed: August 6, 2001)

For: CIRCUIT MODIFICATION)
METHOD)

Group Art Unit: 2825

Examiner: Naum B. Levin

Confirmation No.: 9506

AMENDMENT UNDER 37 C.F.R. §1.111

Assistant Commissioner for Patents
Washington, D.C. 20231

Date: January 21, 2003

Sir:

5# / amdt a
1/25/03
EBU

This amendment responds to the Office Action dated October 24, 2002 (Paper No.

4). Please amend the above noted application as follows:

IN THE CLAIMS:

Please cancel claim 1 without prejudice and replace claims 2, 7, 9 and 10 as follows:

1 2. (Amended) A circuit modification method of modifying a circuit by
inserting one or more buffers into a predetermined wire located within the circuit, the
method comprising the steps of:

determining whether a glitch error is caused in said predetermined wire by one or
more aggressors each comprised of one or more other wires;